

10-05-01
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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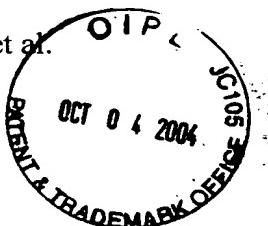
In re Application of:

Anat BREMLER BAR, et al.

Serial No.: 10/774,169

Filed: February 5, 2004

For: DETECTING AND PROTECTING
AGAINST WORM TRAFFIC ON A
NETWORK



) Art Unit: 2155

) Examiner: (N/A)

) Washington, D.C.

) October 4, 2004

) Docket No.: 206,443

INFORMATION DISCLOSURE STATEMENT [IDS]

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This Information Disclosure Statement is submitted in accordance with 37 C.F.R. 1.97, 1.98, and it is requested that the information set forth in this statement and in the listed documents be considered during the pendency of the above-identified application, and any other application relying on the filing date of the above-identified application or cross-referencing it as a related application.

1. This IDS should be considered, in accordance with 37 C.F.R. 1.97, as it is filed:

(Check one of the boxes A-D)

A. within three months of the filing date of the above-identified national application or within three months of the entry into the national stage of the above-identified international application.

B. before the mailing date of a first office action on the merits.

C. after (A) and (B) above, but before final rejection or allowance, and Applicants have made the necessary certification (box "i" below) or paid the necessary fee (box "ii" below).

(check one of the boxes "i" and "ii" below:)

i. Counsel certifies that, upon information and belief, each item of information listed herein was either (a) cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this IDS; or (b) was not cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of undersigned after making reasonable inquiry, was not known to any individual designated in 1.56(c) more than three months prior to the filing of this IDS.

STATEMENT OF FILING BY EXPRESS MAIL 37 C.F.R. § 1.10

This correspondence is being deposited with the United States Postal Service on October 4, 2004 in an envelope as "Express Mail Post Office to Addressee" Mailing Label Number ET 537 591 287 US addressed to the Honorable Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

ii. A check for the fee set forth in 1.17(p), presently believed to be \$240, is enclosed (check no. _____).

D. after (A), (B) and (C) above, but before payment of the issue fee: Applicant petitions under 37 C.F.R. 1.97(d) for the consideration of this IDS. A check for the fee set forth in §1.17(i), presently believed to be \$130 is enclosed (check no. _____). Counsel certifies that, upon information and belief, each item of information listed herein was either (i) cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the IDS; or (ii) was not cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the undersigned after making reasonable inquiry, was not known to any individual designated in 1.56(c) more than three months prior to the filing of this IDS.

2. In accordance with 37 C.F.R. 1.98, this IDS includes a list (e.g., form PTO-1449) of all patents, publications, or other information submitted for consideration by the office, either incorporated into this IDS or as an attachment hereto. A copy of each document listed is attached, except as explained below.

(check boxes A and/or B and fill in blanks, if appropriate.)

A. Document(s) _____ is (are) deemed substantially cumulative to document(s) _____, and, in accordance with 1.98(c), only a copy of each of the latter documents is enclosed.

B. Certain documents were previously cited by or submitted to the Office in the following prior application(s), which are relied upon under 35 U.S.C. 120:

[insert serial numbers and filing dates of prior applications]

Applicant identifies these documents by attaching hereto copies of the forms PTO-892 and PTO-1449 from the files of the prior application(s) or a fresh PTO-1449 listing these documents, and request that they be considered and made of record in accordance with 1.98(d). Per 37 CFR 1.98(d), copies of these documents need not be filed in this application.

3. Documents _____ is (are) not in the English language. In accordance with 1.98(c), Applicant states:

An English translation of each document _____ (or of the pertinent portions thereof), or a copy of each corresponding English-language patent or application, or English-language abstract (or claim) is enclosed.

A concise explanation of the relevance of document(s) _____ is found in the attached search report (see reply to Comment 68 in the preamble to the final rules; 1135 OG 13 at 20).

A concise explanation of the relevance of document(s) _____ is set forth as follows:
[Insert concise explanation of relevance]

A concise explanation of the relevance of document(s) _____ can be found on page(s) _____ of the specification.

A concise explanation of document(s) _____ can be found on the attached sheet.

4. No explanation of relevance is necessary for documents in the English language (see reply to Comments 67 and 68 in the preamble to the final rules; 1135 OG 13 at 20).

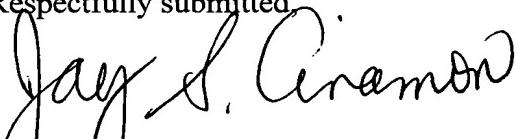
5. Other information being provided for the examiner's consideration follows:

6. In accordance with 37 C.F.R. 1.97(g) and (h), the filing of this IDS should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56 (b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicant reserves the right to prove that the date of publication is in fact different.

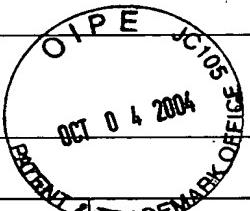
CROSS REFERENCE UNDER 37 C.F.R. §1.78 TO RELATED APPLICATIONS

Pursuant to 37 C.F.R. § 1.78, Applicant notes that the above-identified patent application may be related to the following U.S. Patent Applications:

Respectfully submitted,


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FORM PTO-1449 (Colb)	ATTY DOCKET NO. 206,443	SERIAL NUMBER 10/774,169
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION STATEMENT	APPLICANT Anat BREMLER BAR, et al.	EXAMINER (N/A)
	FILING DATE February 5, 2004	GROUP ART UNIT (N/A)

U.S. PATENT DOCUMENTS

Examiner's Initials		DOCUMENT NO.	DATE	NAME	CLASS	SUB	FILING DATE
	AA	6,397,335	05-2002	Franczek, et al.			
	AB	6,513,122	01-2003	Magdych, et al.			
	AC	2002/0083175	06-2002	Afek et al.			
	AD						
	AE						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB	TRANSLATION
	AF						

OTHER ART (Including Author, Bills, Pertinent Pages, Etc.)

AG	Bennett, J.C.R. et al. "Hierarchical Packet Fair Queueing Algorithms", 1996.
AH	Bennett, J.C.R. et al. "High Speed, Scalable, and Accurate Implementation of Fair Queueing Algorithms in ATM Networks", 1996.
AI	Bennett, J.C.R. et al. "WF2Q: Worst-Case Fair Weighted Fair Queueing", 1996.
AJ	Chiussi, F.M. et al. "Implementing Fair Queueing in ATM Switches: The Discrete-Rate Approach", 1998.
AK	Chiussi, F.M. et al. "Minimum-Delay Self-Clocked Fair Queueing Algorithm for Packet-Switched Networks", 1998.
AL	Demers, A. et al. "Analysis and Simulation of a Fair Queueing Algorithm", 1989 Association for Computing Machinery.

EXAMINER:	DATE CONSIDERED:
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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	AG	Eckhardt, D.A. et al. "Effort-limited Fair (ELF) Scheduling for wireless Networks", IEEE INFOCOM 2000.
	AH	Golestani, S.J. "Networks Delay Analysis of a Class of fair Queueing Algorithms", IEEE Journal on Selected Areas in Communications, Vol. 13, no. 6, August 1995, pp. 1057-1070.
	AI	Golestani, S.J. "A self-Clocked fair Queueing Scheme for Broadband Applications", IEEE 1994, pp. 5c.1.1-5c1.11.
	AJ	Greenberg, Albert G. et al. "How Fair is Fair Queuing?" Journal of the Association for Computing Machinery Vol. 39, no. 3, July 1992, pp. 568-598.
	AK	Parekh, A.K.J. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks", Ph.D. Dissertation Massachusetts Institute of Technology, February 1992.
	AL	Parekh, A.K.J. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks: The Multiple Node Case", IEEE/ACM Transactions on Networking Vol. 2, no. 2, April 1994, pp. 137-150.

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	AF						

OTHER ART (Including Author, Bills, Pertinent Pages, Etc.)

	AG	Parekh, A.K.J. "A Generalized Processor Sharing Approach to Flow Control in Integrated Services Networks: The Single-Node Case", IEEE/ACM Transactions on Networking Vol. 1, no. 3, June 1993, pp. 344-357.
	AH	"Quality of Service Networking", downloaded from the web (address: http://www.cisco.com/univercd/cc/td/doc/cisintwk/ito_doc/qos.htm), Cisco Systems, Inc. 2002.
	AI	Rexford, J.L. et al. "Hardware Efficient Fair Queueing Architectures for high-Speed networks", IEEE 1996, pp. 5d.2.1-5d.2.9.
	AJ	Shreedhar M. et al. "Efficient Fair Queueing Using Deficit Round-Robin", IEEE/ACM Transactions on networking Vol. 4, no. 3, June 1996, pp. 375-385.
	AK	Stiliadis, D. et al. "Frame-Based Fair Queueing: A New Traffic Scheduling Algorithm for Packet-Switched Networks", July 1995, pp. 1-43.
	AL	

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